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EXAMINER

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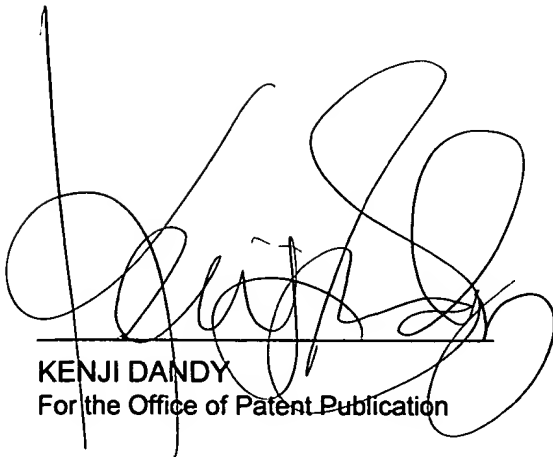
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060705-1610Serial No.
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INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Applicant
Helfenstein, et al.Filing Date
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2631

U.S. PATENT DOCUMENTS

Examiner Initials	Item	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation
							Yes No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

H.V.	A	An Integrated Adaptive Analog Balancing Hybrid For Use In (A) DSL Modems, Frédéric Pécourt, Jörg Hauptmann, Aner Tenen, 1999 IEEE International Solid-State Circuits Conference, ISSCC99/Session 14 Paper TP 14.8, February 16, 1999, pages 252-253, 466.					
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